# DILIP BOIDYA

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## Technical Skills

**Softwares/Technologies**: HTML5, CSS, JavaScript, TypeScript, ReactJS, MongoDB, Postgres, NodeJS, Express, Git & GitHub, npm, docker

**Developer Tools/Extensions**: VS Code, Thunder Client, Chrome Developer Tools, Postman

## Soft Skills

**Collaboration**: Worked effectively in teams during internships and final year project contributing to project success.

**Communication**: Clear and concise in explaining technical concepts to both peers and non-technical audiences.

**Problem-solving**: Proactively solved challenges during development projects.

## Education

| **Tezpur University** | **Aug 2018 – Jun 2022** |
|---|---|
| *Bachelor of Technology in Electronics and Communication Engineering* | *Tezpur, Assam* |

### Relevant Coursework

- Data Structures
- Digital Electronics
- Introductory Computing
- Analog Electronics
- Basic Electronics
- Computer Network

## Experience

| **Intern at Startup** | **Jun 2024 - Sep 2024** |
|---|---|
| *Akrabhi Software Pvt. Ltd.* | *Remote* |

- Developed a full-stack productivity tool to manage bookmarks.
- Backend Engineer

| **Freelancing Project** | **Jun 2024 - Present** |
|---|---|
| *Winger Seat Reservation Web App* | *https://ratan.vercel.app* |

- Developing a full-stack seat reservation web app for Winger using the MERN stack within the Next.js framework.

| **Open Source Contribution** | **Jan 2024 - Feb 2024** |
|---|---|
| *Chingu* | *Remote* |

- Task Management App The project seeks to develop a powerful and user-friendly platform that enables individuals to successfully plan, manage, and complete their chores with precision.
  https://task-craft.netlify.app/ and https://github.com/chingu-voyages/v47-tier2-team-11

| **Fellowship, FOSSEE team @ IIT Bombay** | **May 2022 - Jul 2022** |
|---|---|
| *Electronics Engineer* | *Mumbai, India* |

- An Open Source EDA tool, capable of circuit design, simulation & device modeling, modeled devices such as Diodes, LEDs, Transmission lines, etc.

## Acheivements

**Hackathon: Cloud based Analog IC design hackathon** | *EDA, Synopsys Custom Compiler*    **Duration: 3 weeks**
- The hackathon was jointly conducted by IITH and VSD Pvt Ltd, powered by Synopsys.
- I have designed an LNA, my IP was under "Excellent" category.
  https://github.com/dilipboidya/LNA$_I$$ITH_HACKATHON$

**Marathon: Mixed Signal Circuit Design and Simulation** |    **Duration: 3 weeks**
- A step up converter has been designed using eSim and verilog tool, marathon was conducted by IITB, my IP was considered in "Excellent" category.
  https://github.com/dilipboidya/boost-converter

**Marathon: Circuit Design & Simulation using eSim EDA tool** | *eSim, circuit design*    **Duration: 2 weeks**
- A buck converter was designed using the eSim tool with Skywater 130nm tech, organized by FOSSEE, IITB, and VSD Pvt. Ltd. My IP was considered in the "Very Good" category.
  https://github.com/dilipboidya/buck-converter

## Projects

**QandA- A QnA platform for learners** | *ReactJS, NodeJS, MongoDB, Express*    **Duration: 2 months (and ongoing)**
- Developed a comprehensive QnA platform using the MERN stack
- The project includes two platforms: one tailored for learners and another for Subject Matter Experts (SMEs).
  Learner Platform: https://studyqanda.netlify.app SMEs Platform: https://expertqanda.netlify.app.

**B.Tech Final Year Project** | *CST, PCB Fabrication, soldering, VNA*    **Duration: 1 Year**
- Design, Simulation & Fab of Microstrip Line Directional Coupler.
  *Awarded (3rd prize) Prof. Bipin Dhekial Phukan Memorial Award 2022, for our final year project.*

https://omgelectric.blogspot.com/2023/02/microwave-directional-coupler-in.html